

## FIG.

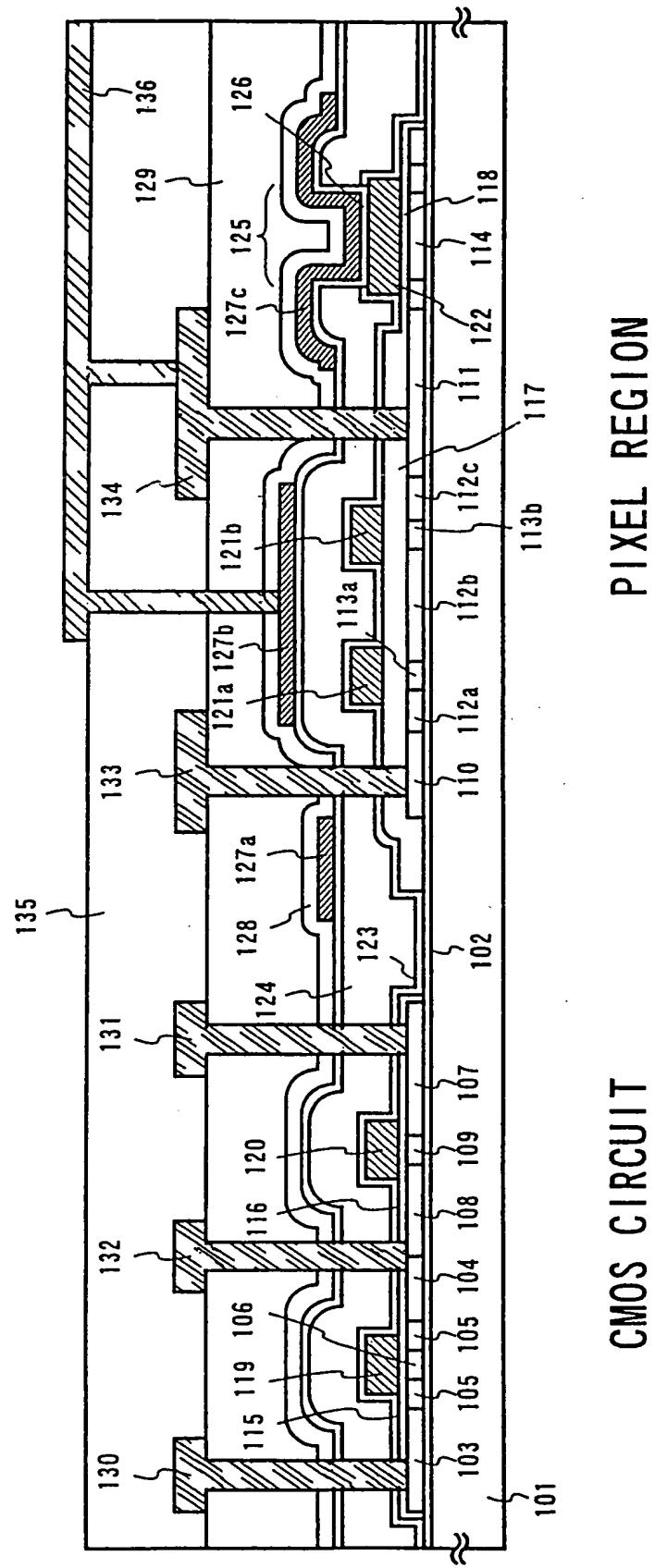


FIG. 2A

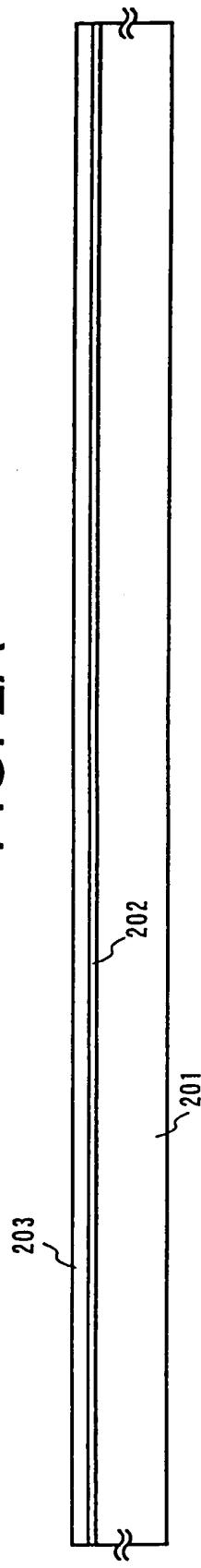


FIG. 2B

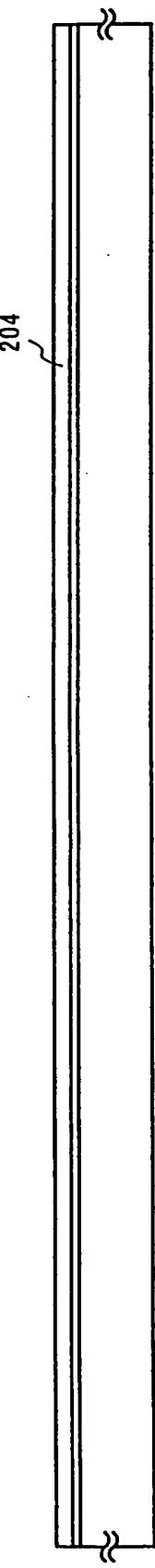


FIG. 2C

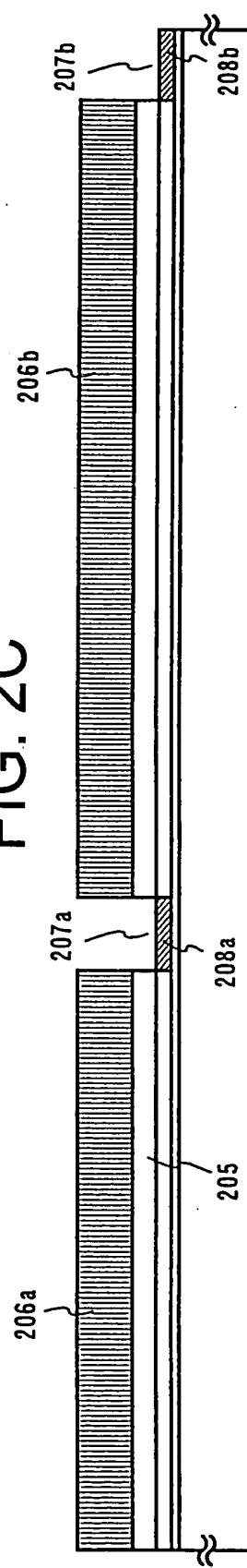


FIG. 2D



FIG. 3A

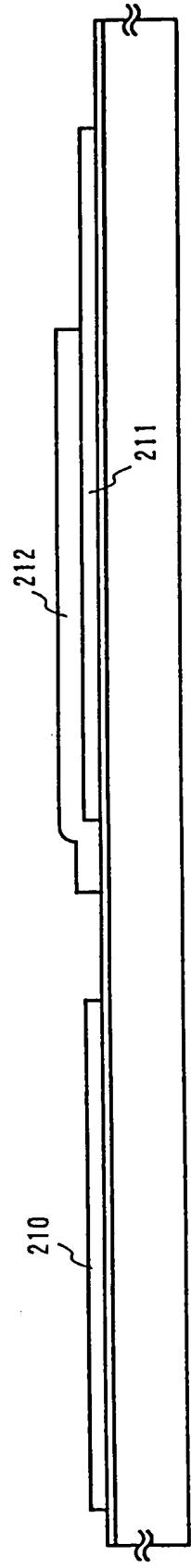


FIG. 3B

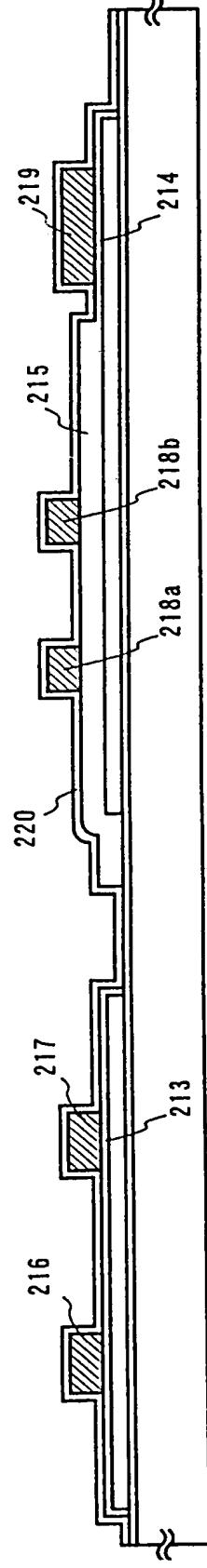


FIG. 3C

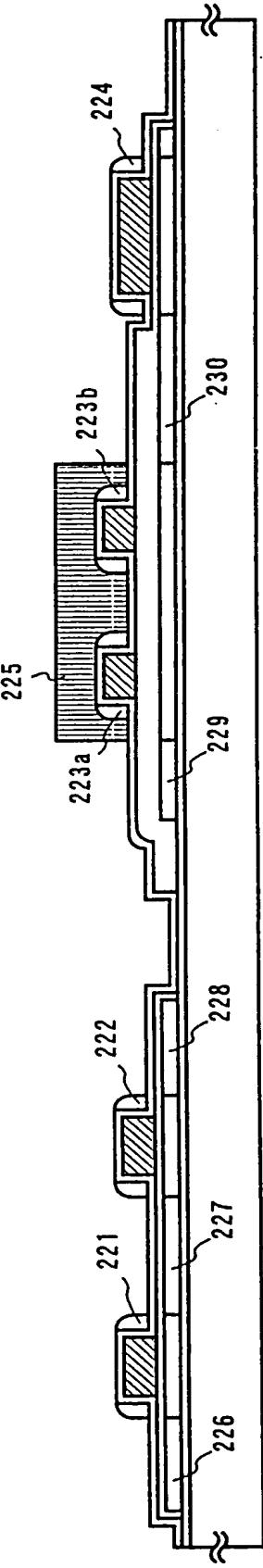


FIG. 3D

FIG. 4A

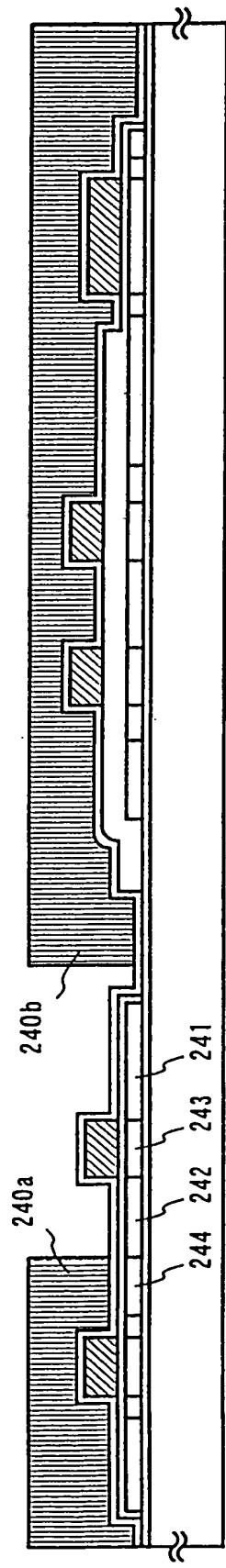


FIG. 4B

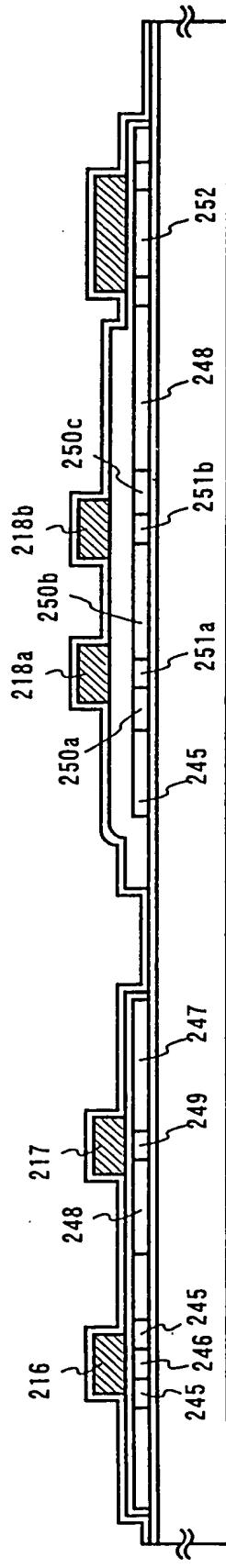


FIG. 4C

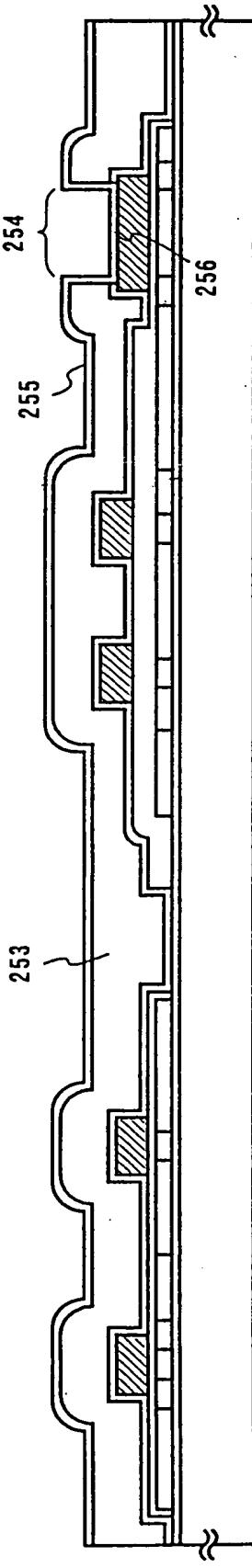


FIG. 5A

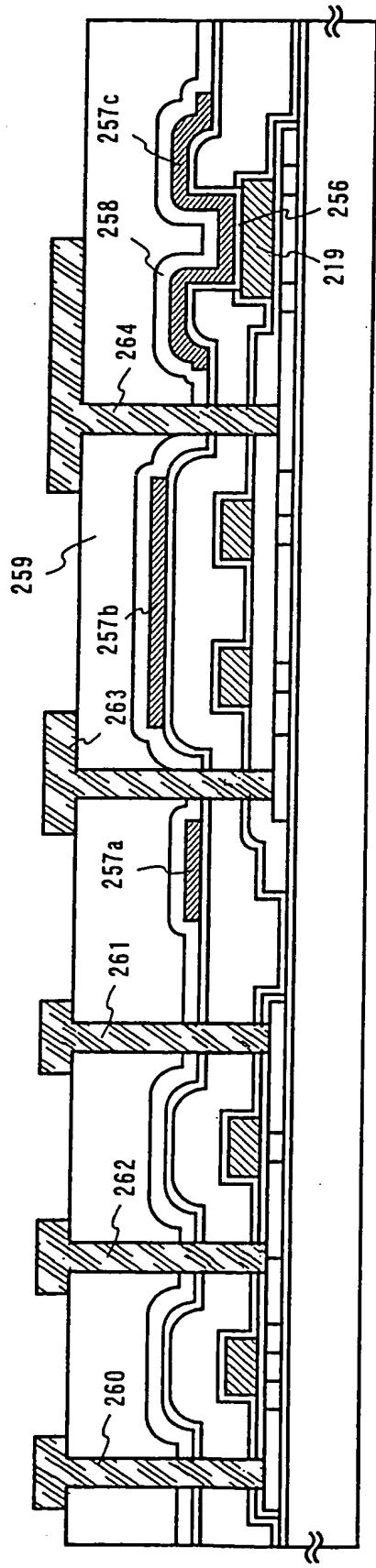
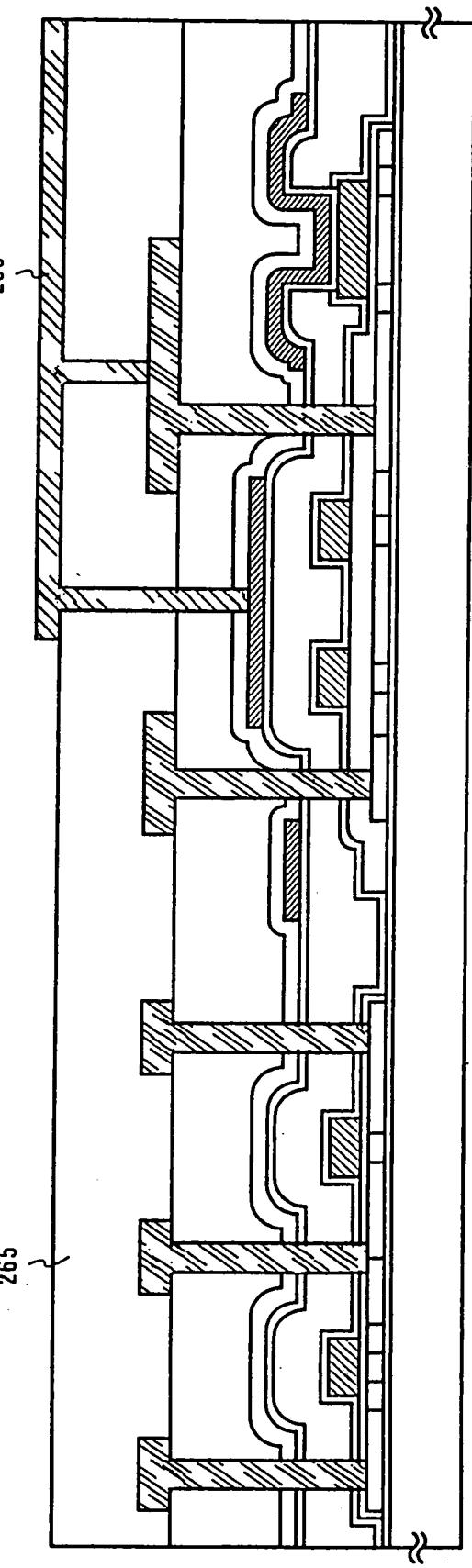


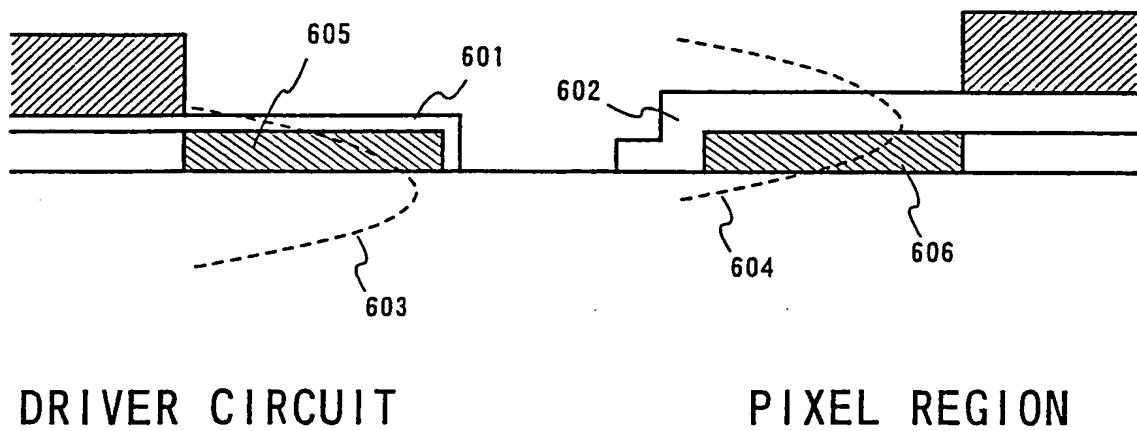
FIG. 5B



CMOS CIRCUIT

PIXEL REGION

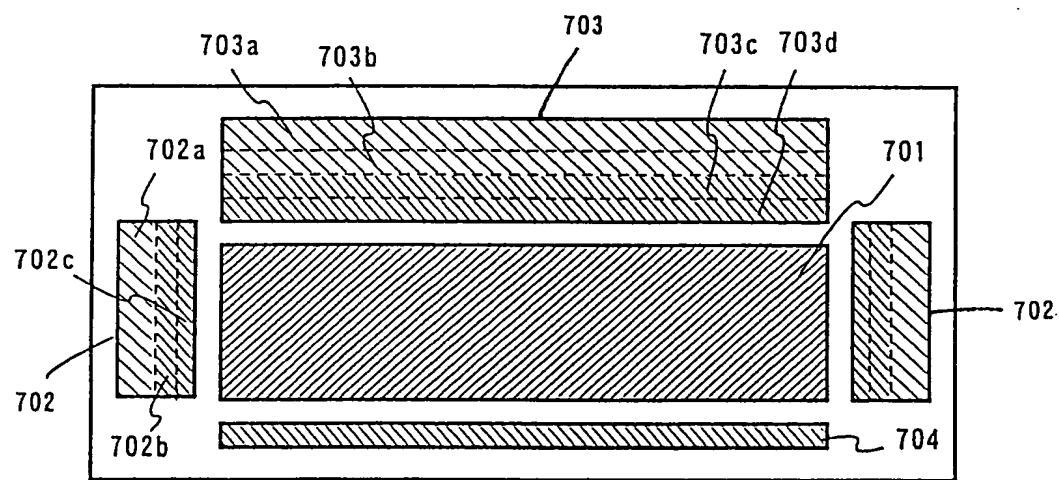
FIG. 6



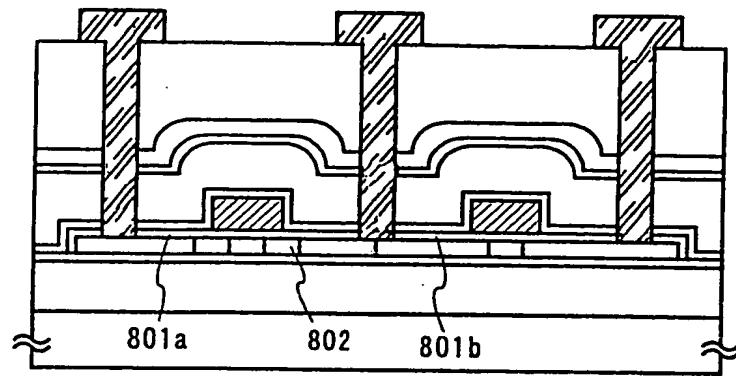
DRIVER CIRCUIT

PIXEL REGION

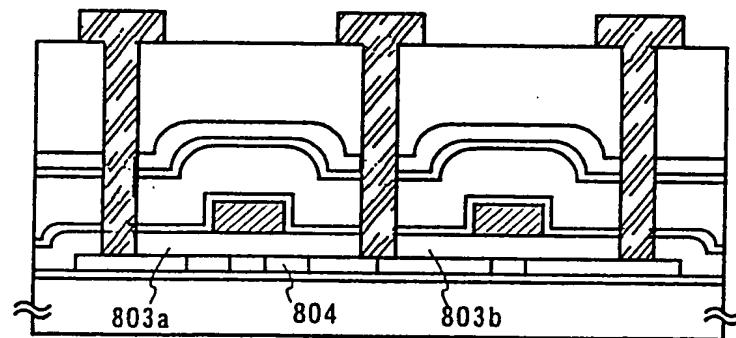
FIG. 7



**FIG. 8A**



**FIG. 8B**



**FIG. 8C**

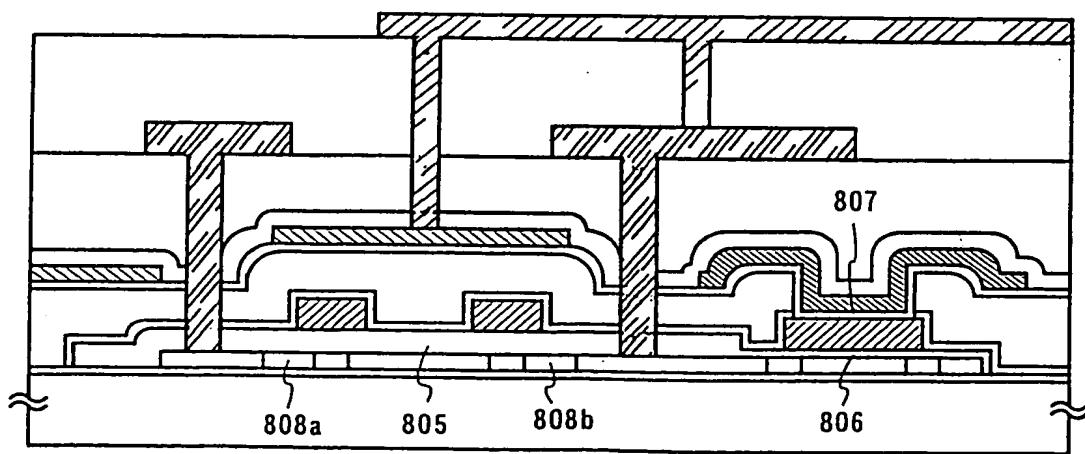
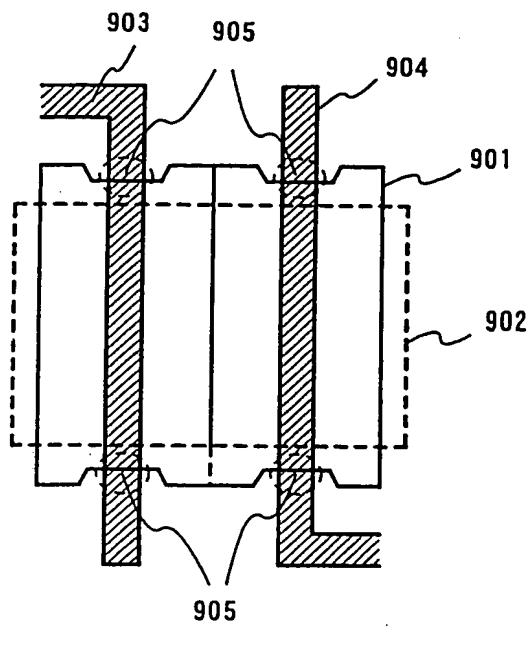
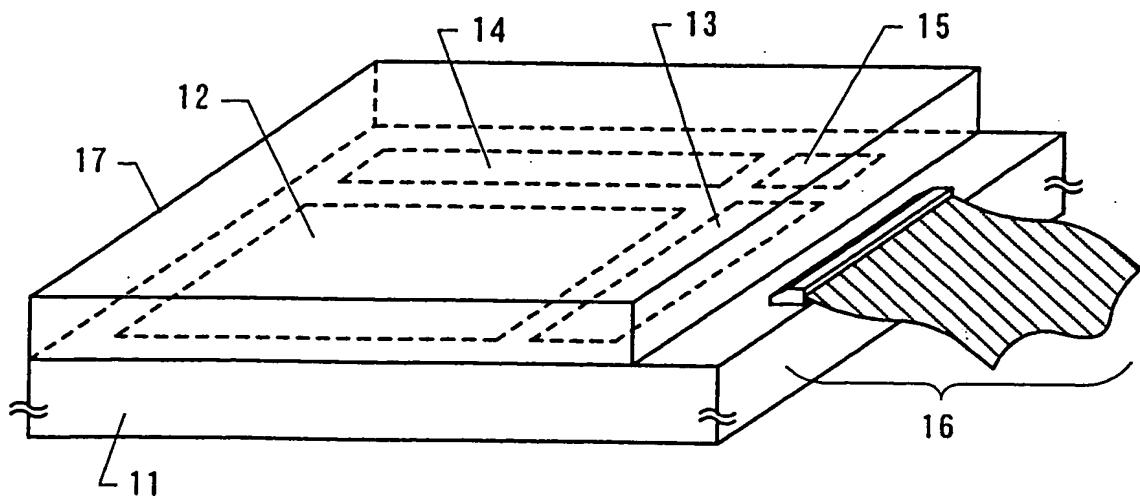


FIG. 9

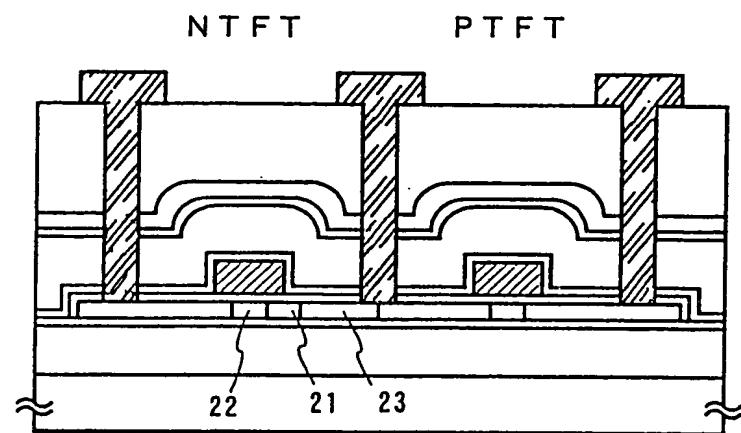


NTFT      PTFT

FIG. 10



**FIG. 11A**



**FIG. 11B**

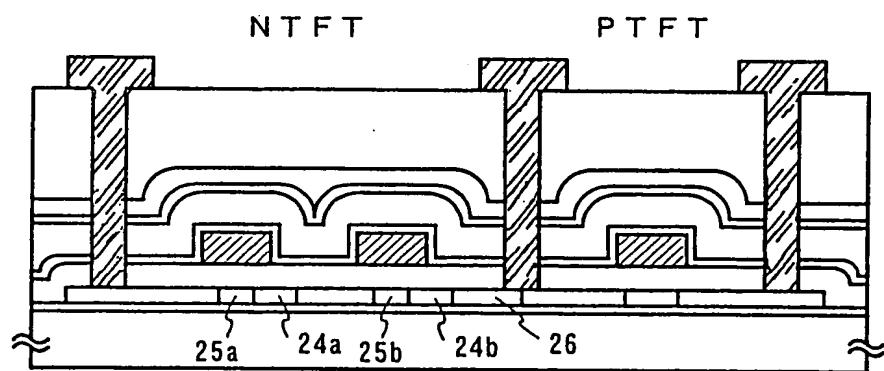


FIG. 12

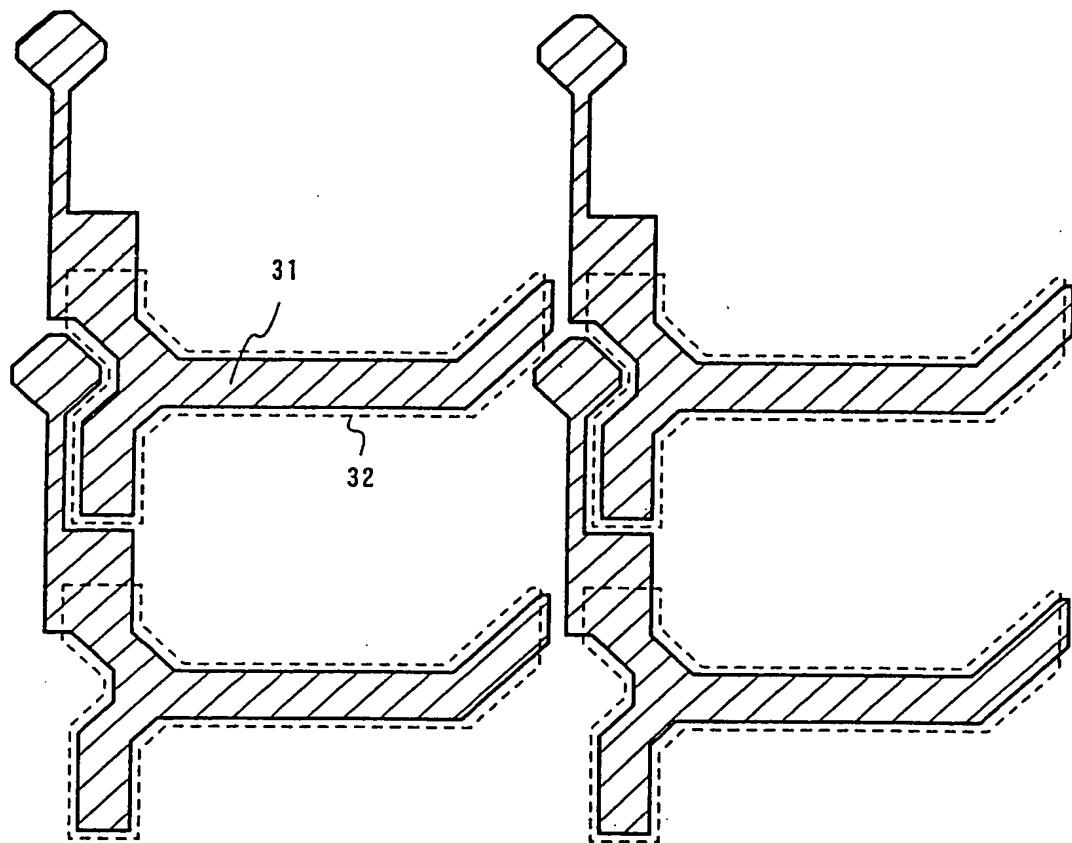


FIG. 13

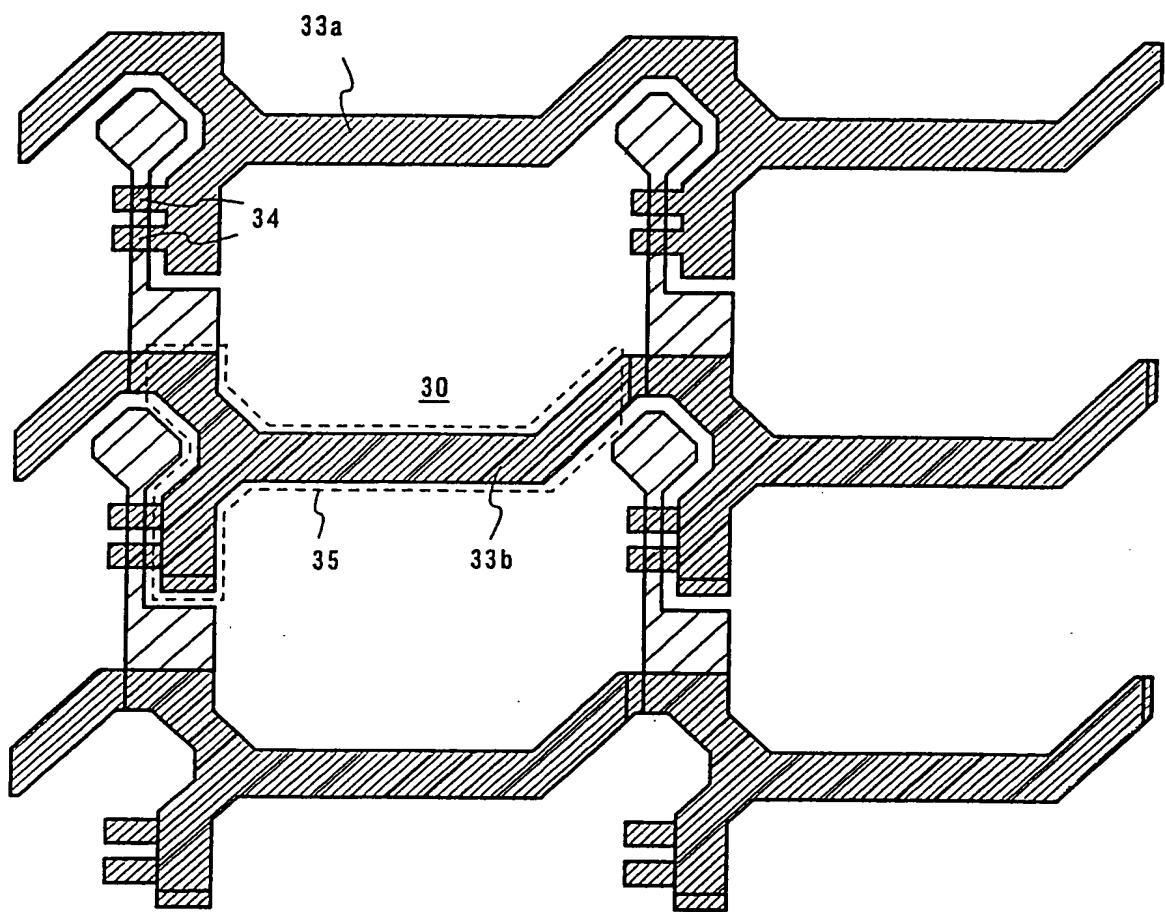


FIG. 14

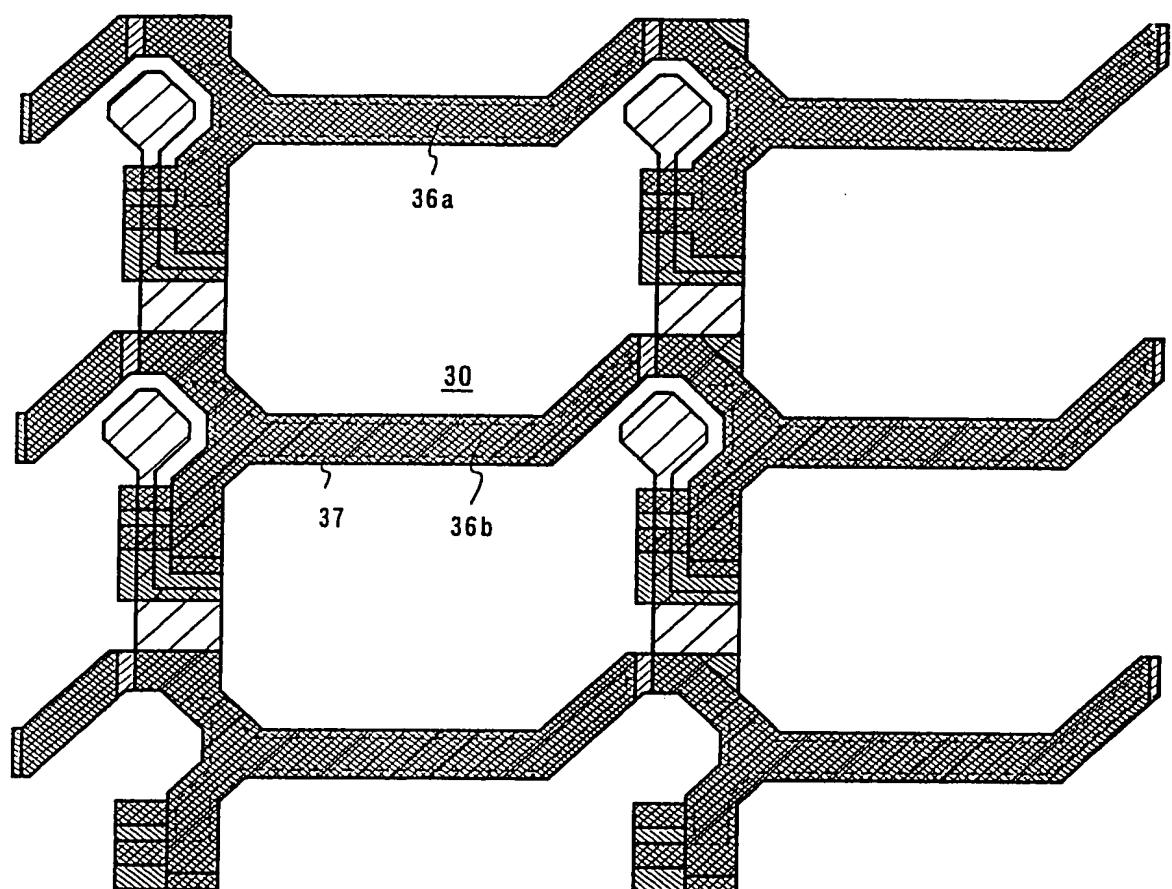


FIG. 15

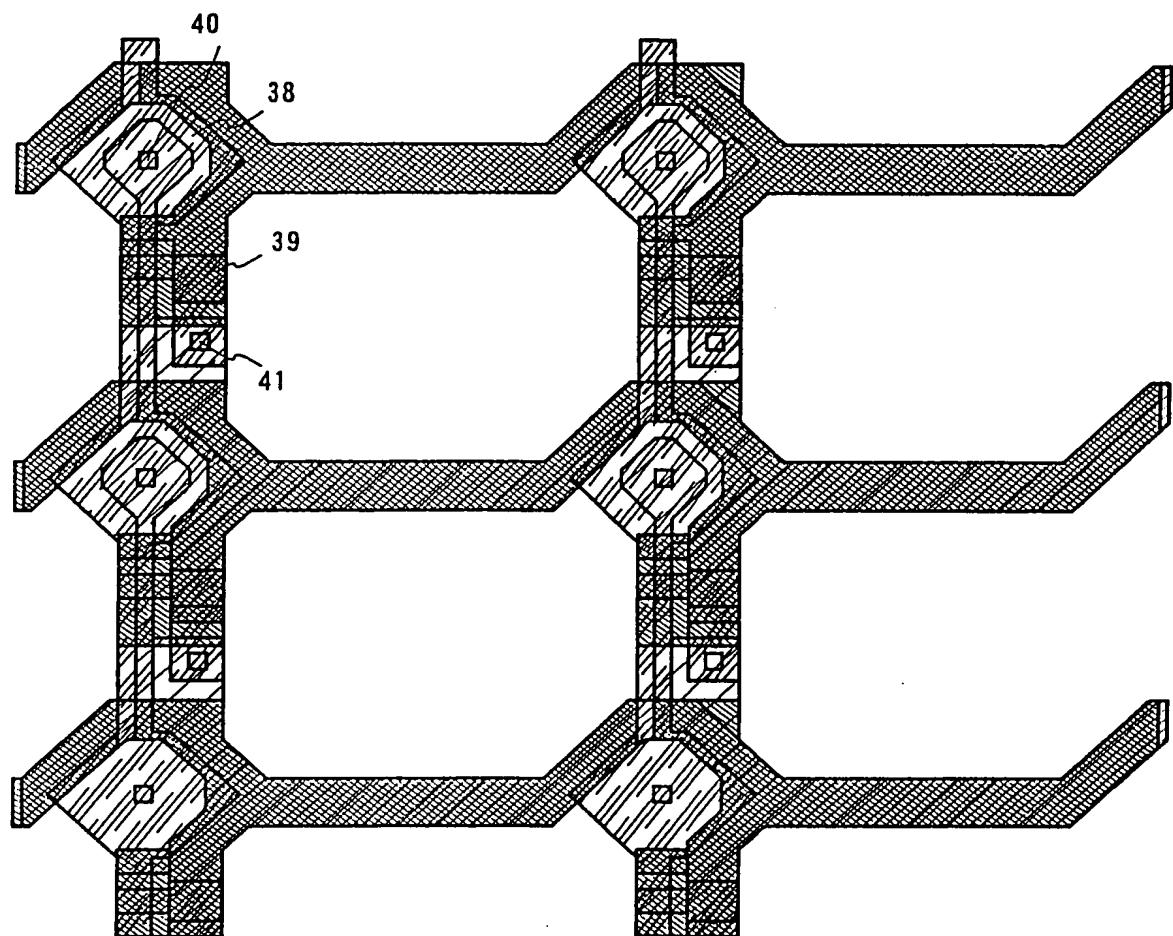


FIG. 16

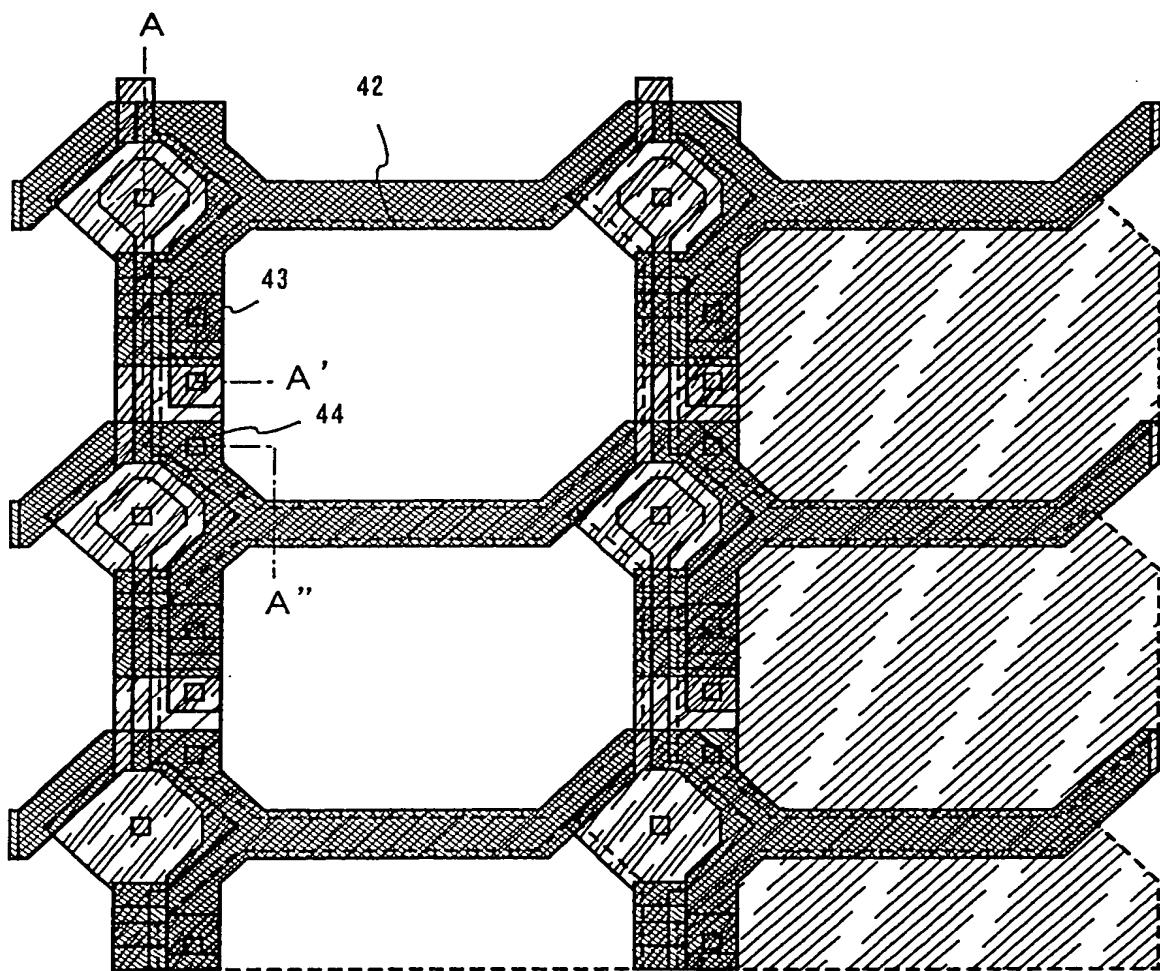


FIG. 17A

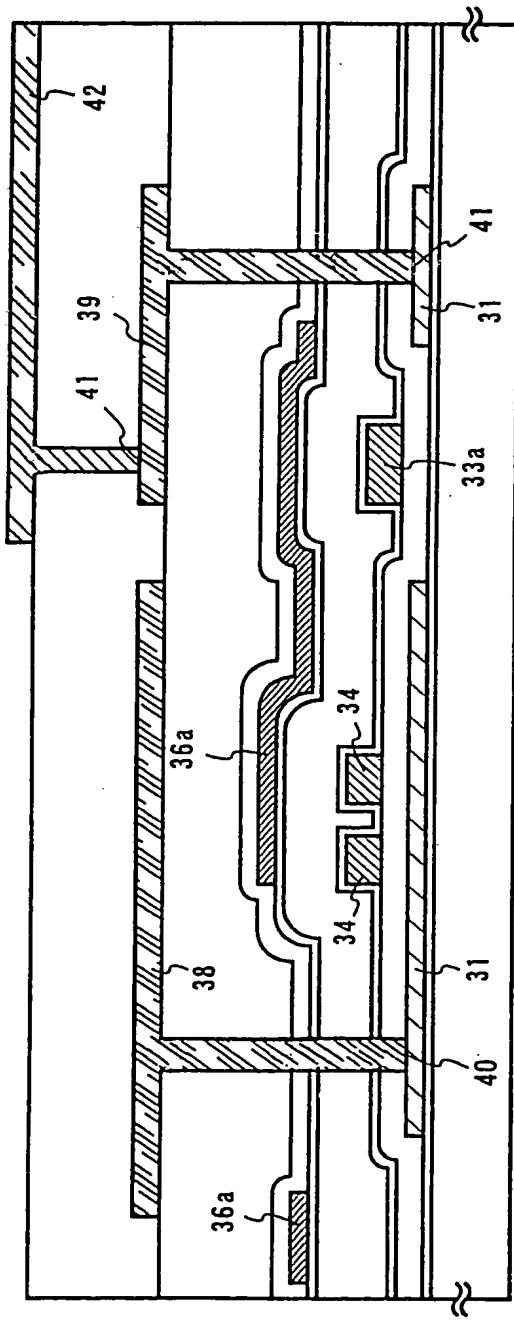


FIG. 17B

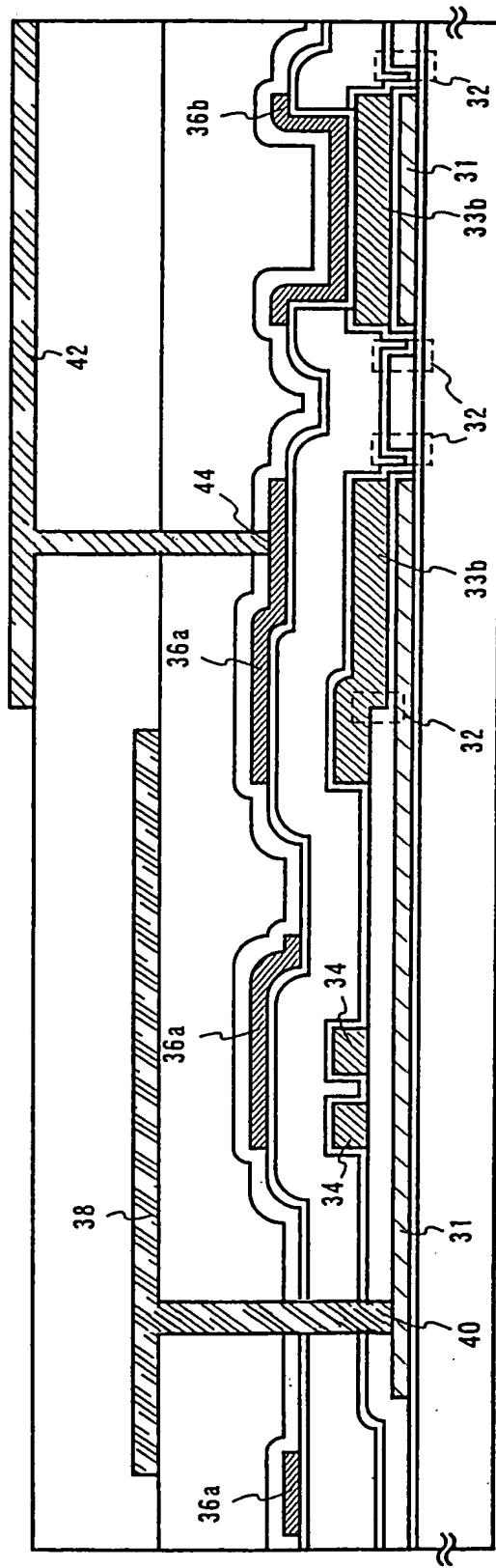


FIG. 18A

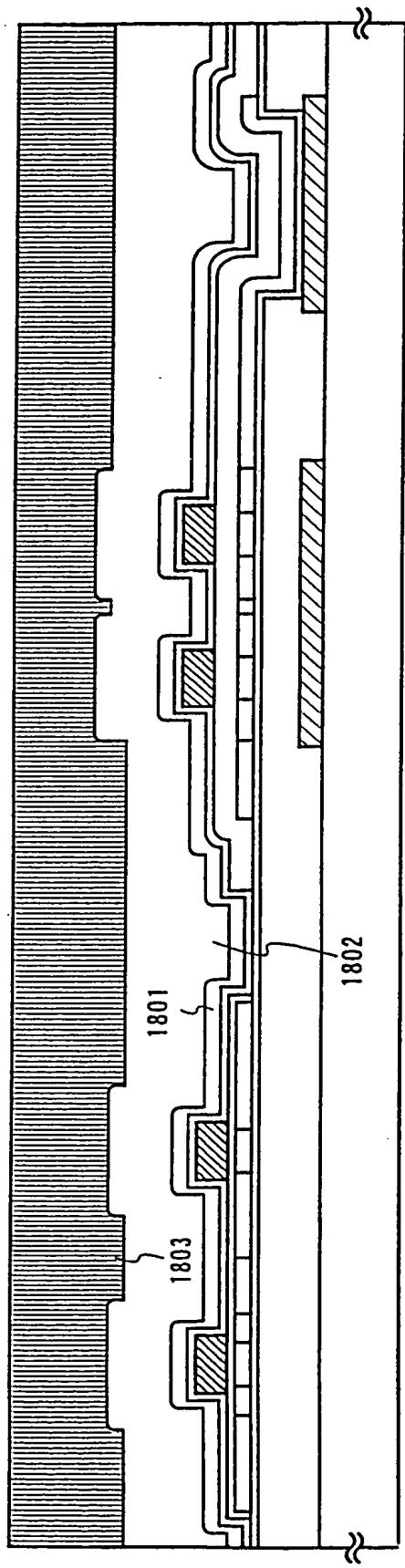


FIG. 18B

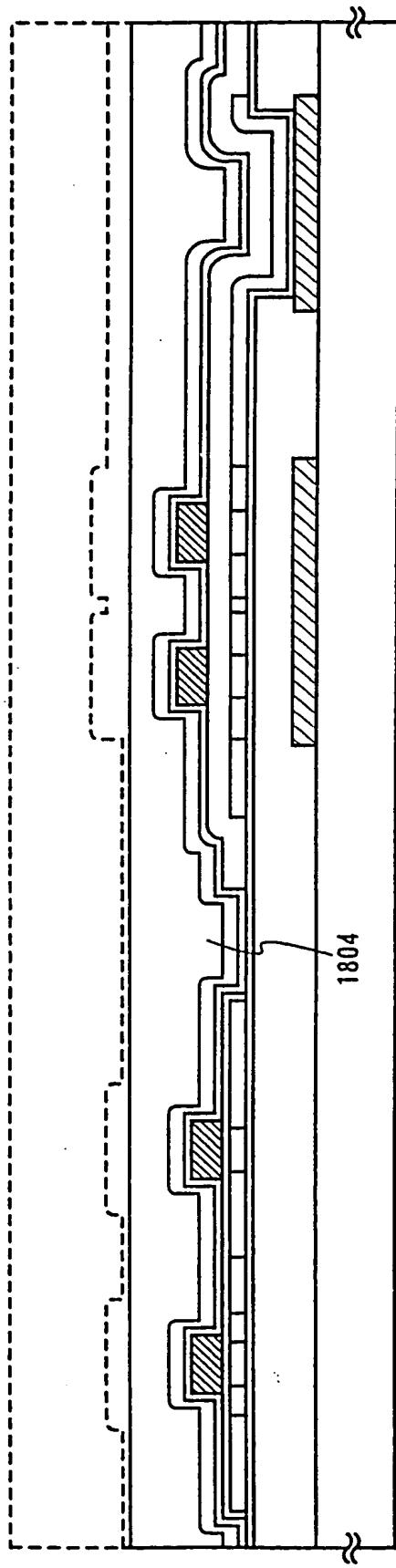


FIG. 19

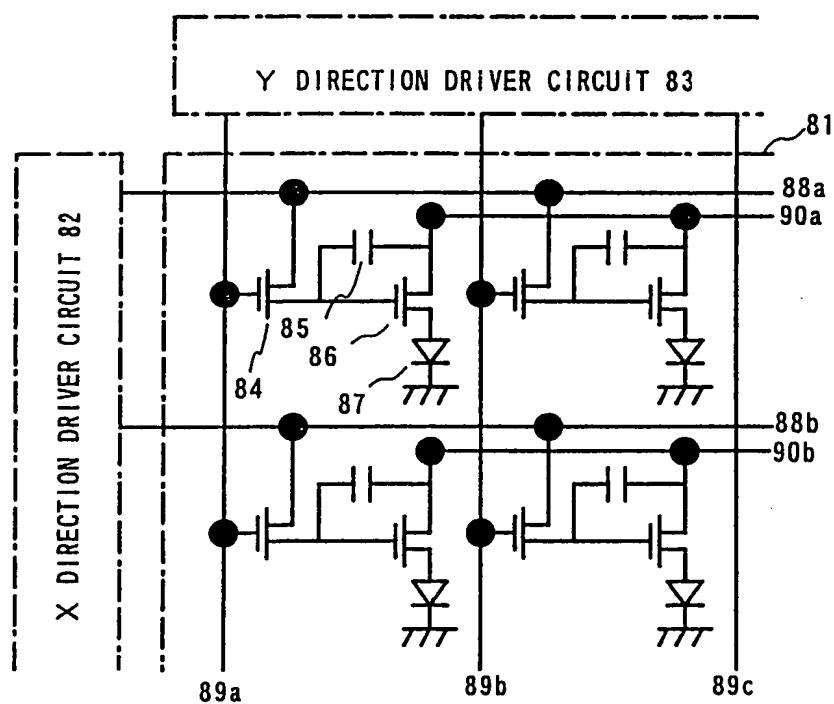


FIG. 20A

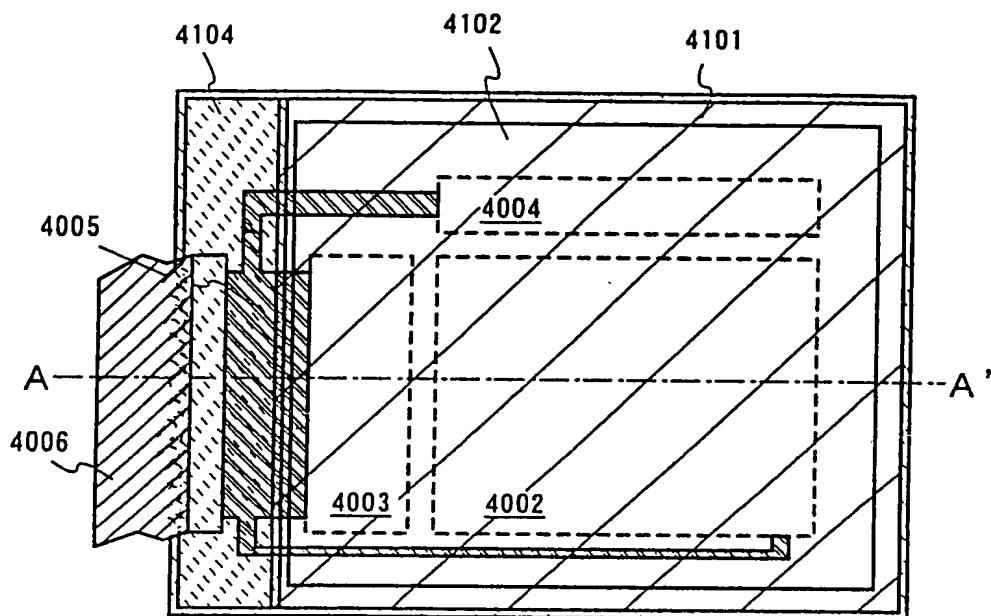
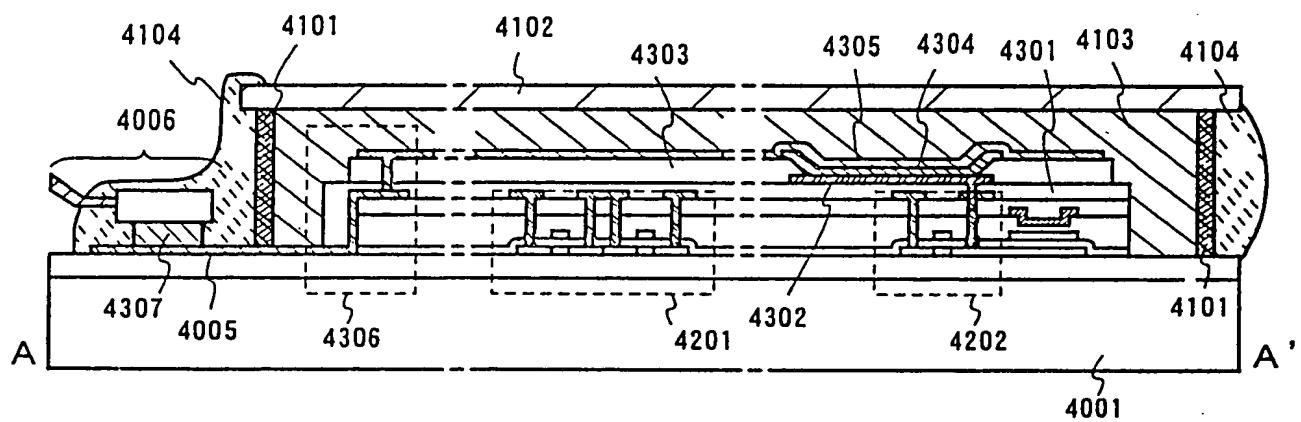
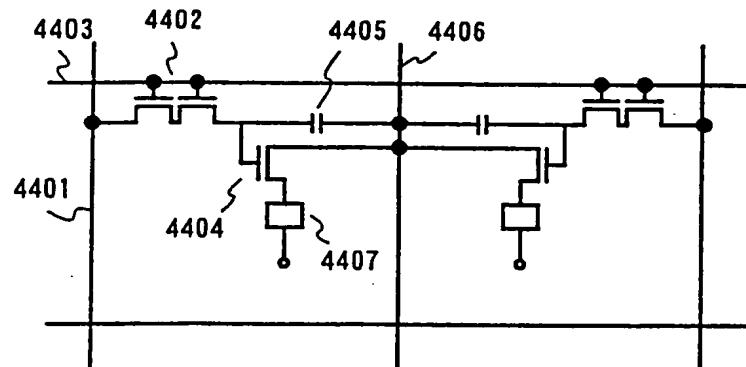


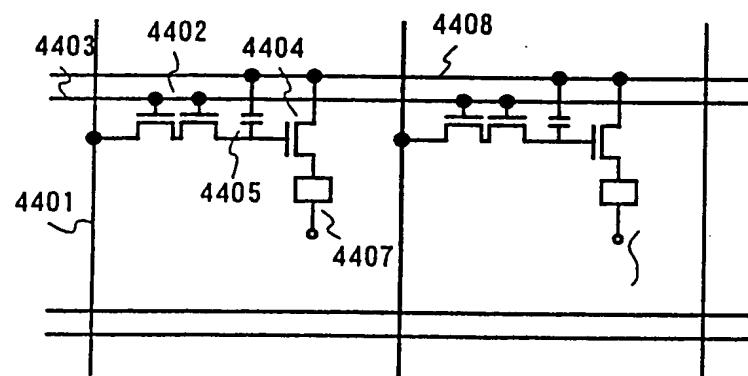
FIG. 20B



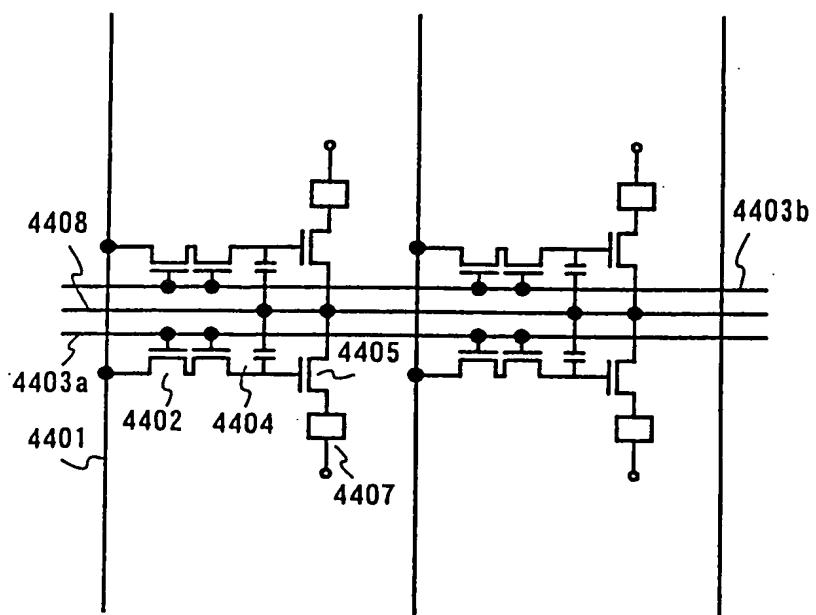
**FIG. 21A**



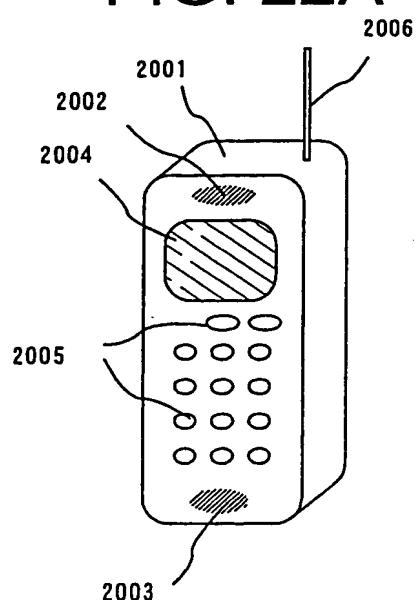
**FIG. 21B**



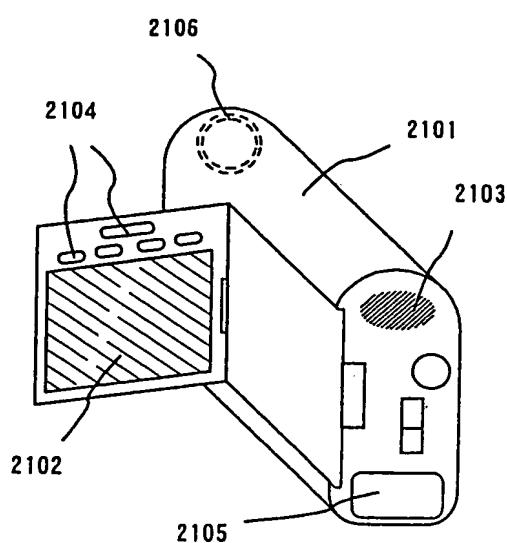
**FIG. 21C**



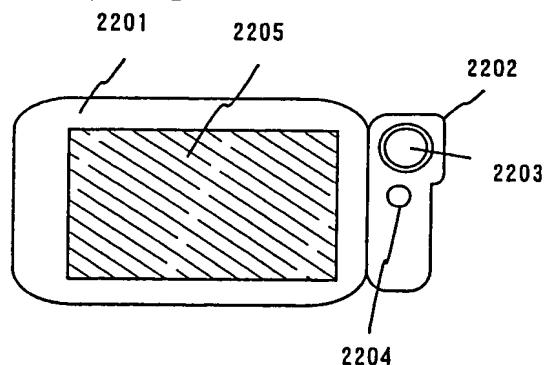
**FIG. 22A**



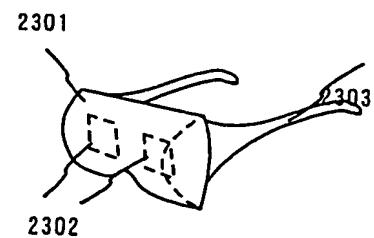
**FIG. 22B**



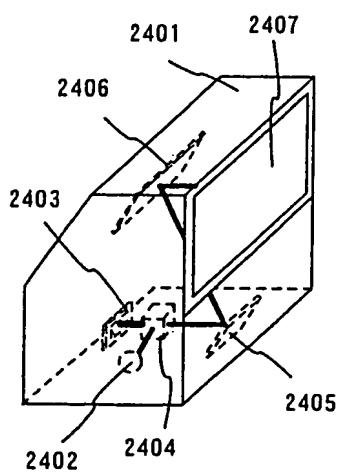
**FIG. 22C**



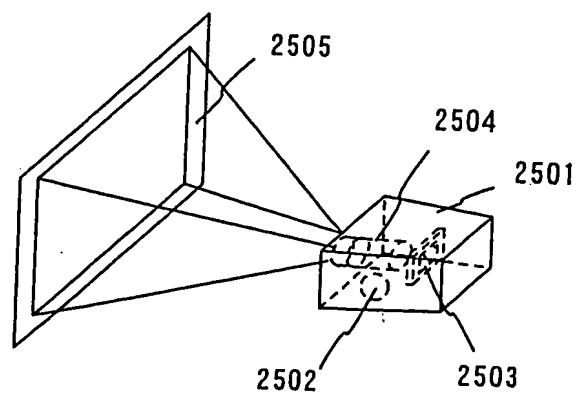
**FIG. 22D**



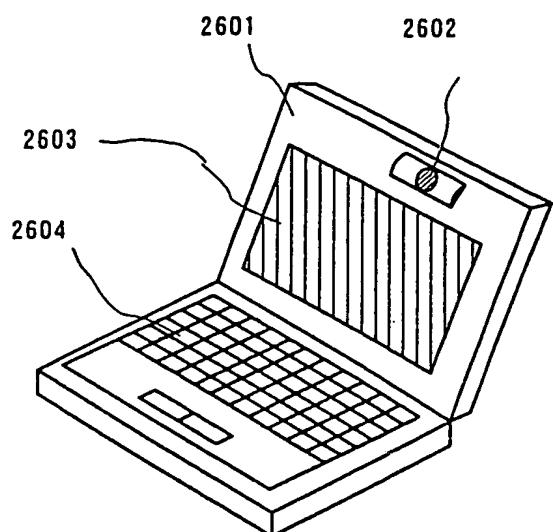
**FIG. 22E**



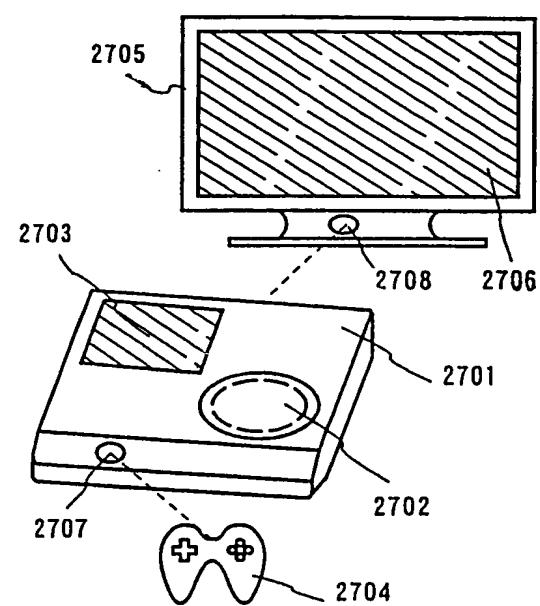
**FIG. 22F**



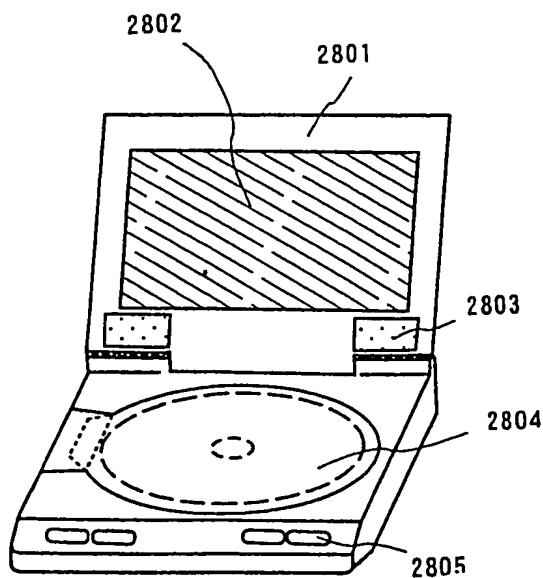
**FIG. 23A**



**FIG. 23B**



**FIG. 23C**



**FIG. 23D**

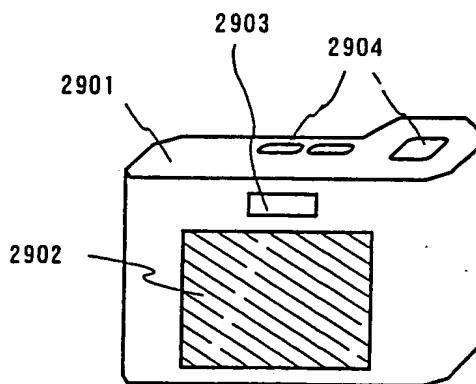


FIG. 24A

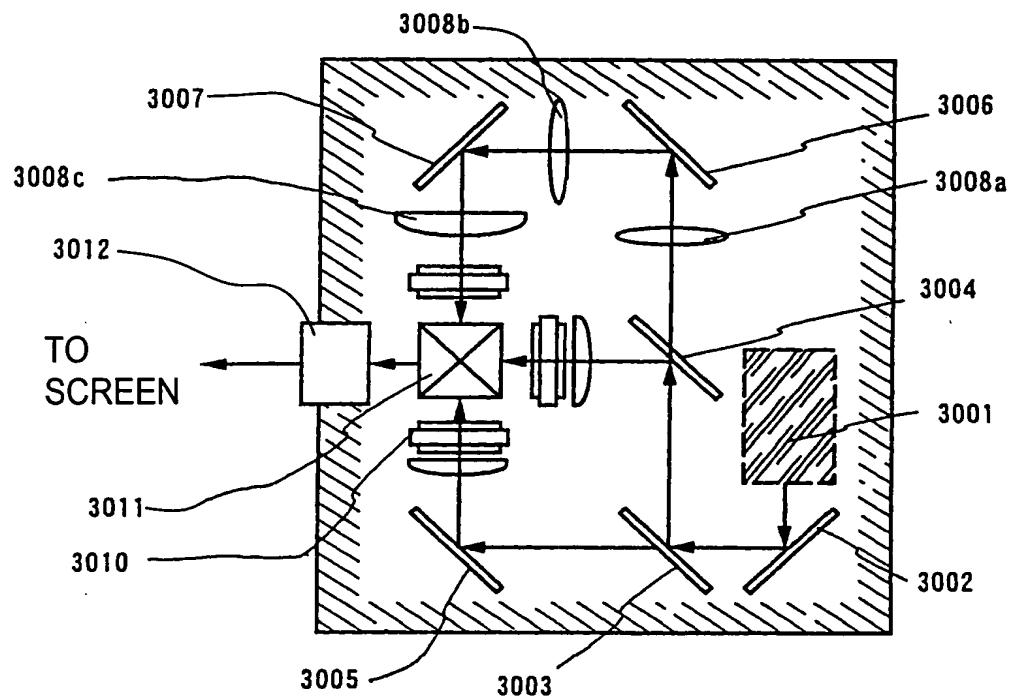


FIG. 24B

